

CIRCUIT LAYOUT ARRANGEMENT FOR KEY SWITCH SIGNAL RECOGNITION

ABSTRACT OF THE DISCLOSURE

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A key switch signal recognition circuit including a plurality of key switch buttons, a plurality of key switch signal output lines, and a plurality of key switch button contact areas is disclosed. Each of the key switch contact areas includes a first wire set and a second wire set. The first wire set has at least one wire, and each wire of the first wire set has one end connected to a common line and an opposite end forming an open end. The second wire set has at least one wire respectively disposed in parallel to and electrically insulated from the wire of the first wire set. The wire of the second wire set is respectively connected to a key switch signal output line. When depressing one key switch button to touch the corresponding key switch button contact area, the electrically conductive element of the depressed key switch button electrically connects the first wire set and second wire set of the touched key switch contact area, causing the respective key switch signal output line to send a key switch signal corresponding to the depressed key switch button.